Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S46	1	10/643876	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/20 19:23
S47	1	symmetric near select with transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 08:21
S48	30	symmetric near select	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:47
S49	1	S48 and transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:46
S50	157	symmetric near transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:47
S51	0	symmetric near (transistor with select$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:47
S52	1063	symmetric with transistor near5select$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 11:50
S53	30	symmetric with transistor near5 select$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:53
S54	45554	(memory adj cell) near3 array	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:54
S55	17828	polysilicon adj gate	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:55

S56	54	S55 with select adj transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:55
S57	0	S54 with S56 with perpendicuar$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:56
S58	0	S56 with perpendicuar$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:56
S59	3	S56 with perpendicular$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:56
S60	2	S54 with S56 with perpendicular$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/20 19:56
S61	16	symmetric with select with transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 08:22
S62	0	("6430077").URPN.	USPAT	OR	OFF	2005/10/25 08:23

S63	105	("3895360" | "4016588" | "4017888" | "4151021" | "4173766" | "4173791" | "4281397" | "4306353" | "4342149" | "4360900" | "4380057" | "4388705" | "4389705" | "4471373" | "4527257" | "4586163" | "4630085" | "4667217" | "4780424" | "4847808" | "4870470" | "4916671" | "4941028" | "5021999" | "5075245" | "5117389" | "5159570" | "5168334" | "5172338" | "5175120" | "5204835" | "5214303" | "5241497" | "5260593" | "5268861" | "5289412" | "5293563" | "5305262" | "5311049" | "5315541" | "5345425" | "5349221" | "5350710" | "5359554" | "5393701" | "5394355" | "5399891" | "5412601" | "5414693" | "5418176" | "5418743" | "5422844" | "5424978" | "5426605" | "5434825" | "5450341" | "5450354" | "5467308" | "5477499" | "5495440" | "5496753" | "5521870" | "5523251" | "5553018" | "5583808" | "5599727" | "5623438" | "5654568" | "5656513" | "5661060" | "5683925" | "5712814" | "5726946" | "5751037" | "5754475" | "5777919" | "5787036" | "5793079" | "5812449" | "5825686" | "5834851" | "5836772" | "5841700" | "5847441" | "5862076" | "5864164" | "5870335" | "5946558" | "5949728" | "5963412" | "5963465" | "5966603" | "5973373" | "5990526" | "5991202" | "6018186" | "6020241" | "6028324" | "6030871" | "6034403" | "6063666" | "6128226" | "6134156" | "6137718" | "6163048").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/25 08:24
S64	40	("5963465").URPN.	USPAT	OR	OFF	2005/10/25 08:47
S65	7	("4281397" | "5204835" | "5315541" | "5495440" | "5583808" | "5623438" | "5777919").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/25 08:47
S66	4169	(257/314-316).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/25 11:48

S67	4344	(438/257-265).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/25 11:48
S68	2832	(365/185.05,185.12,185.17,185.21, 185.23).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/10/25 11:49
S69	22	S66 and symmetric with transistor near5select$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 11:52
S70	12	S67 and symmetric with transistor near5select$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 11:51
S71	13	S68 and symmetric with transistor near5select$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/25 11:50

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	1	symmetric near3 transistors and polysilicon near gates and perpendicular$4 near3 memory adj cell.clm.	US-PGPUB	OR	ON	2005/10/25 14:26
L8	32	symmetric and transistors and select and polysilicon and gates and perpendicular$4 and memory and cell.clm.	US-PGPUB	OR	ON	2005/10/25 15:37